

Writes Using ARRs to
direct request to
desired resource

Input uses BARs to recognize
I/O Address as being directed
to local resource

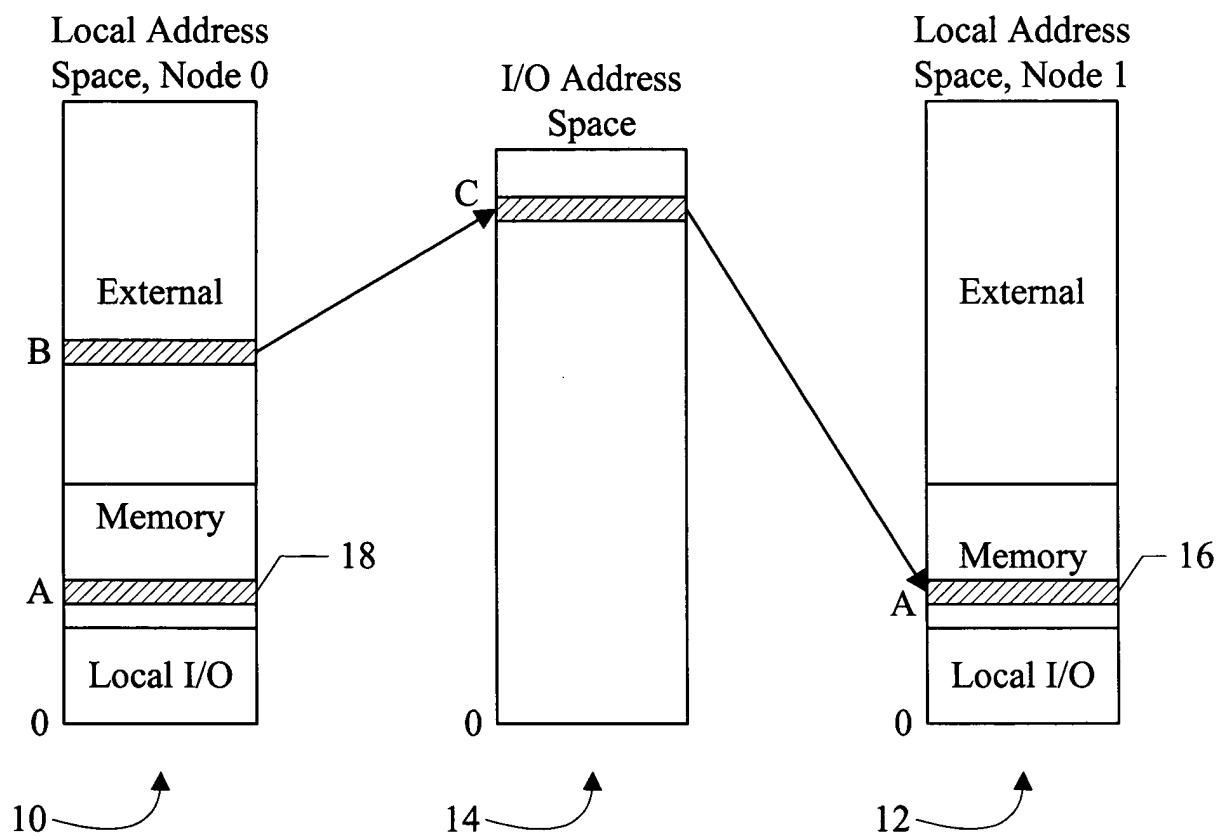


Fig. 1A
(Prior Art)

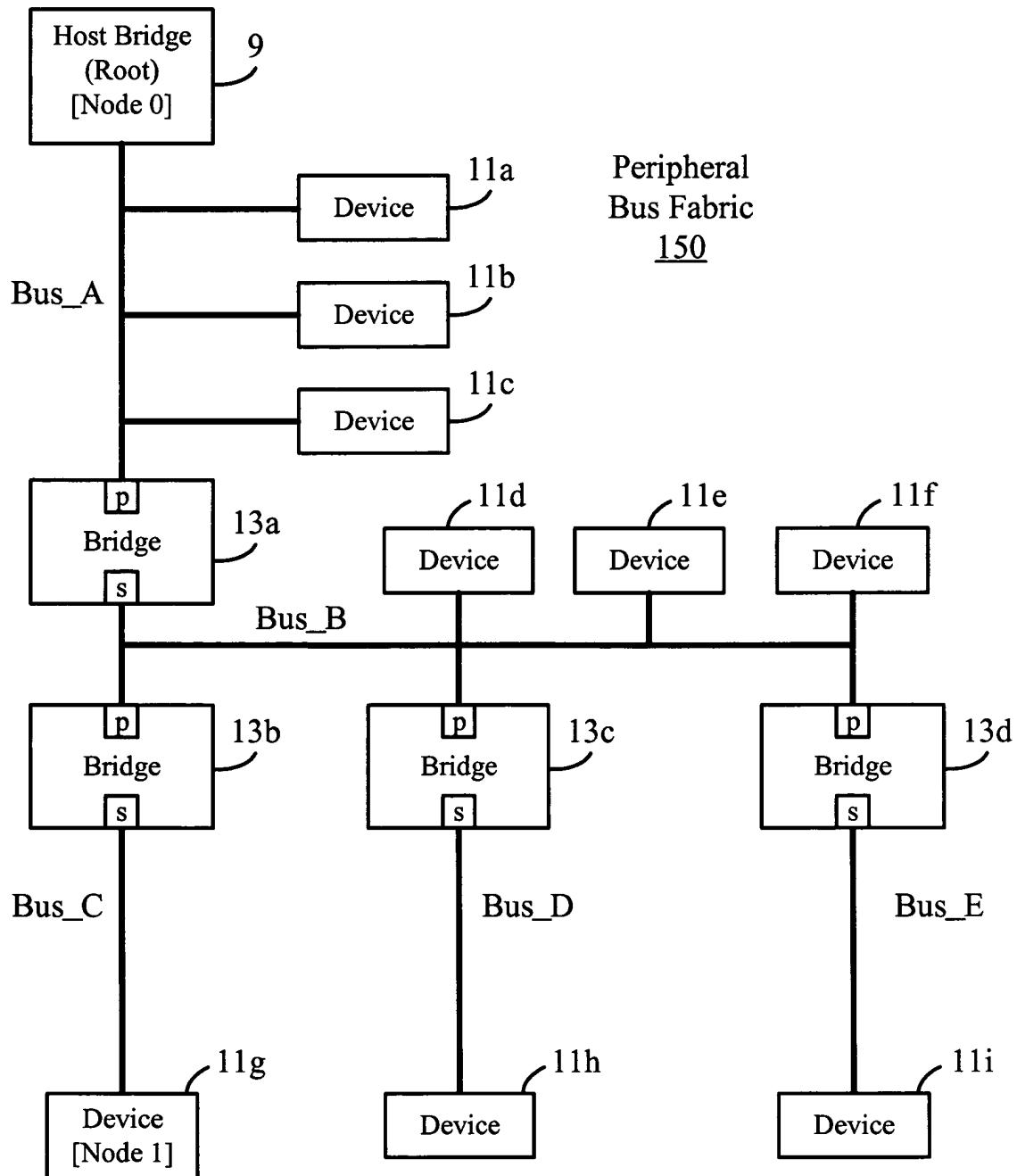
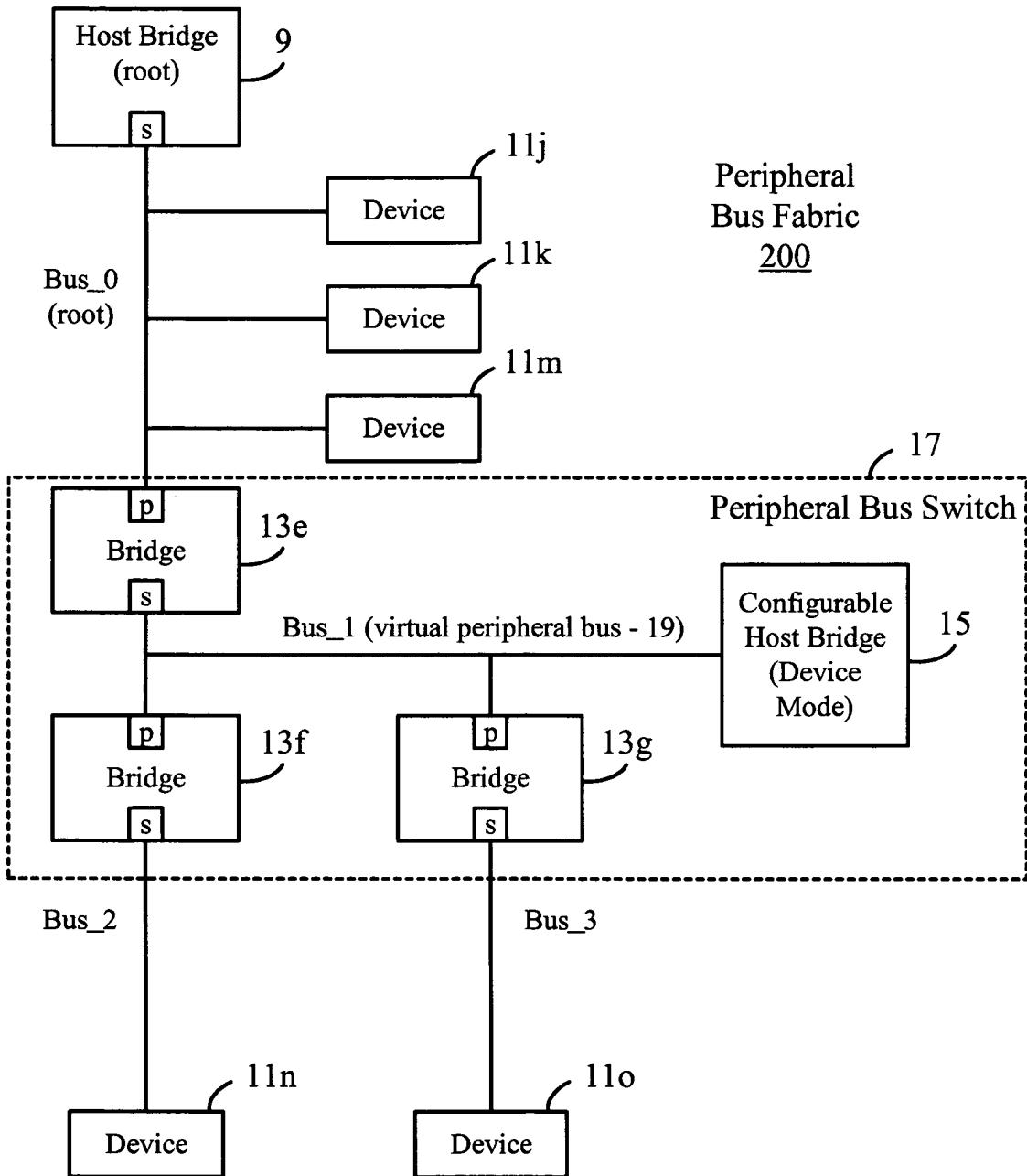


Fig. 1B
(Prior Art)

Fig. 2A

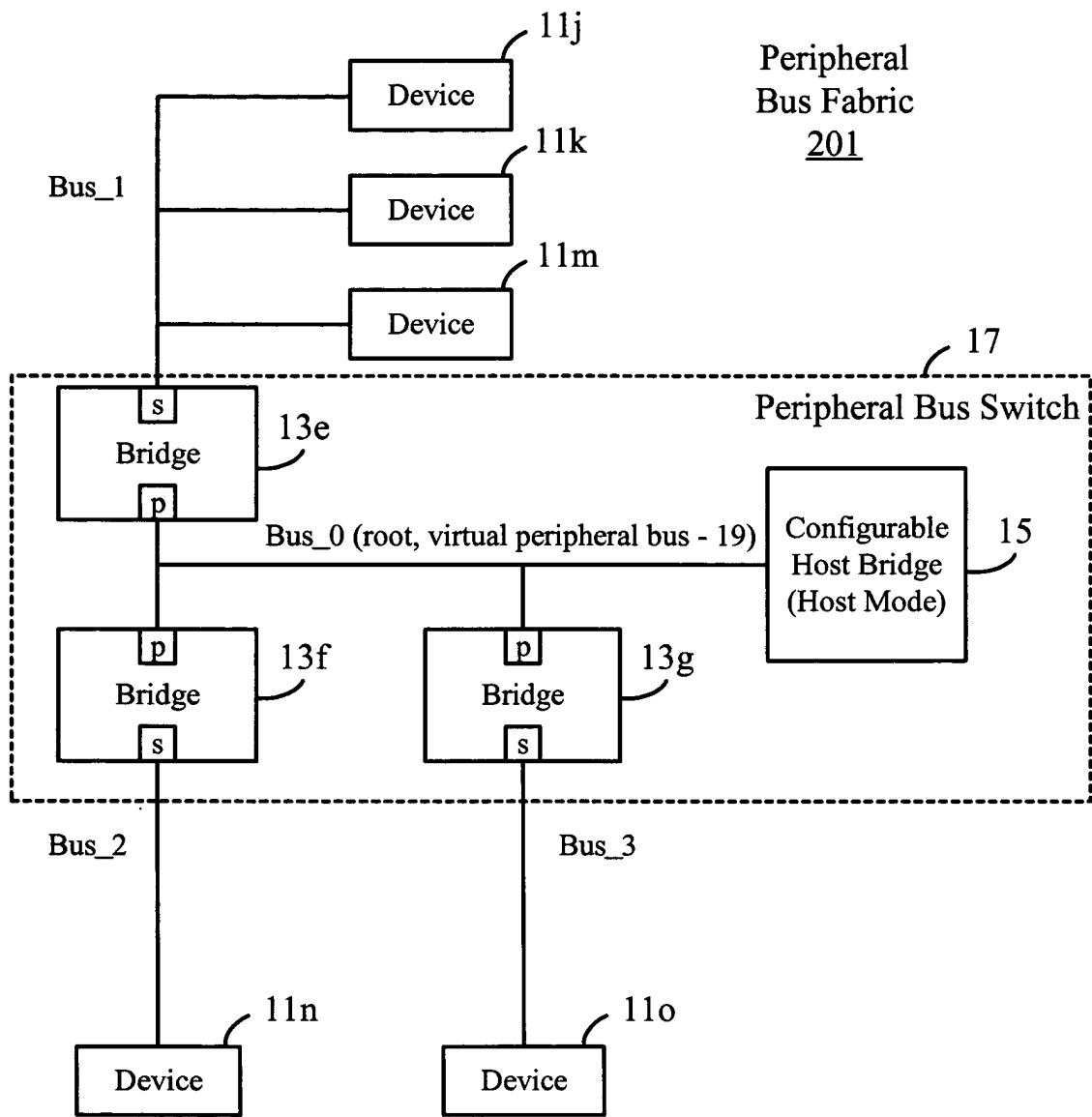


Fig. 2B

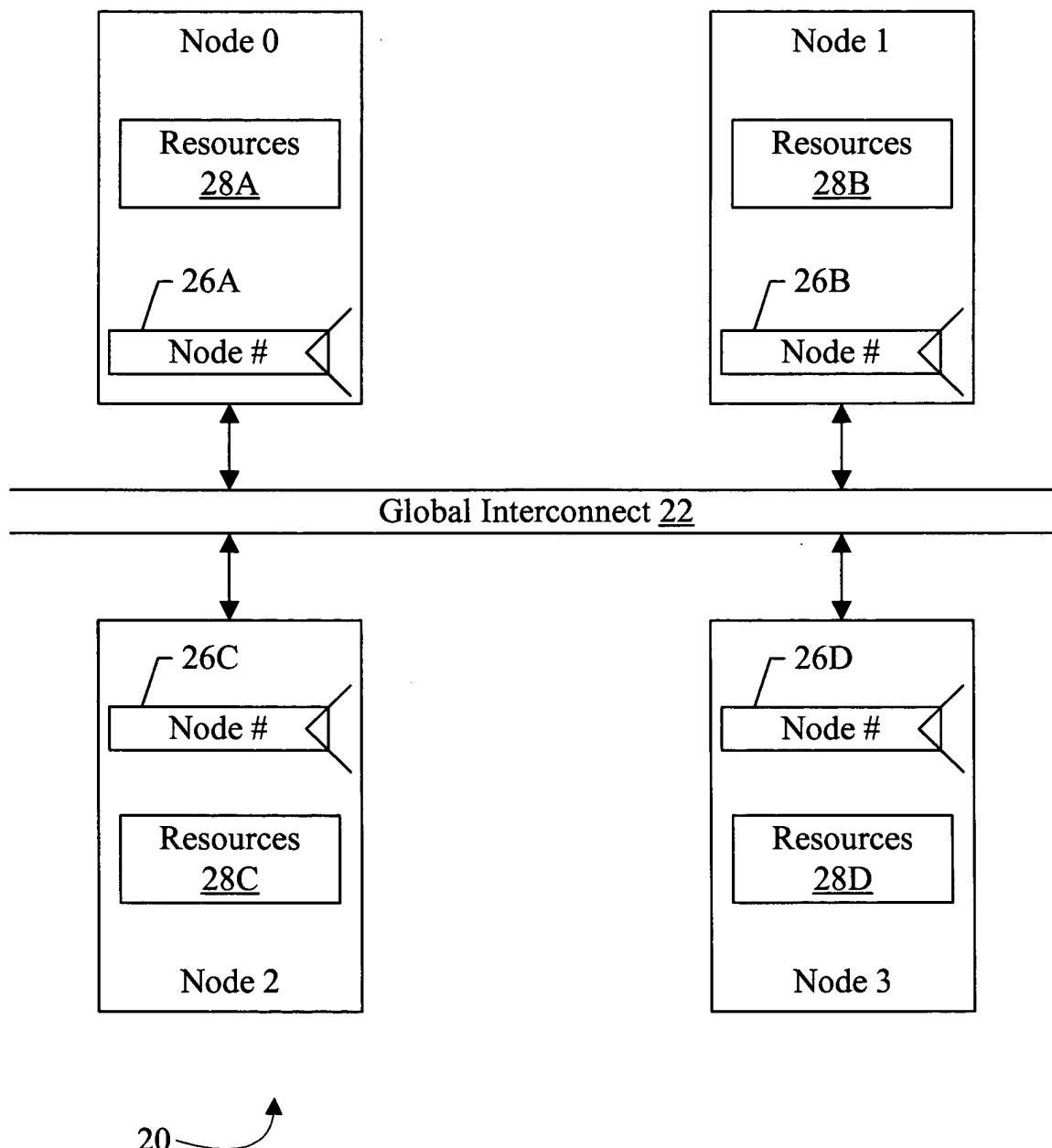


Fig. 2C

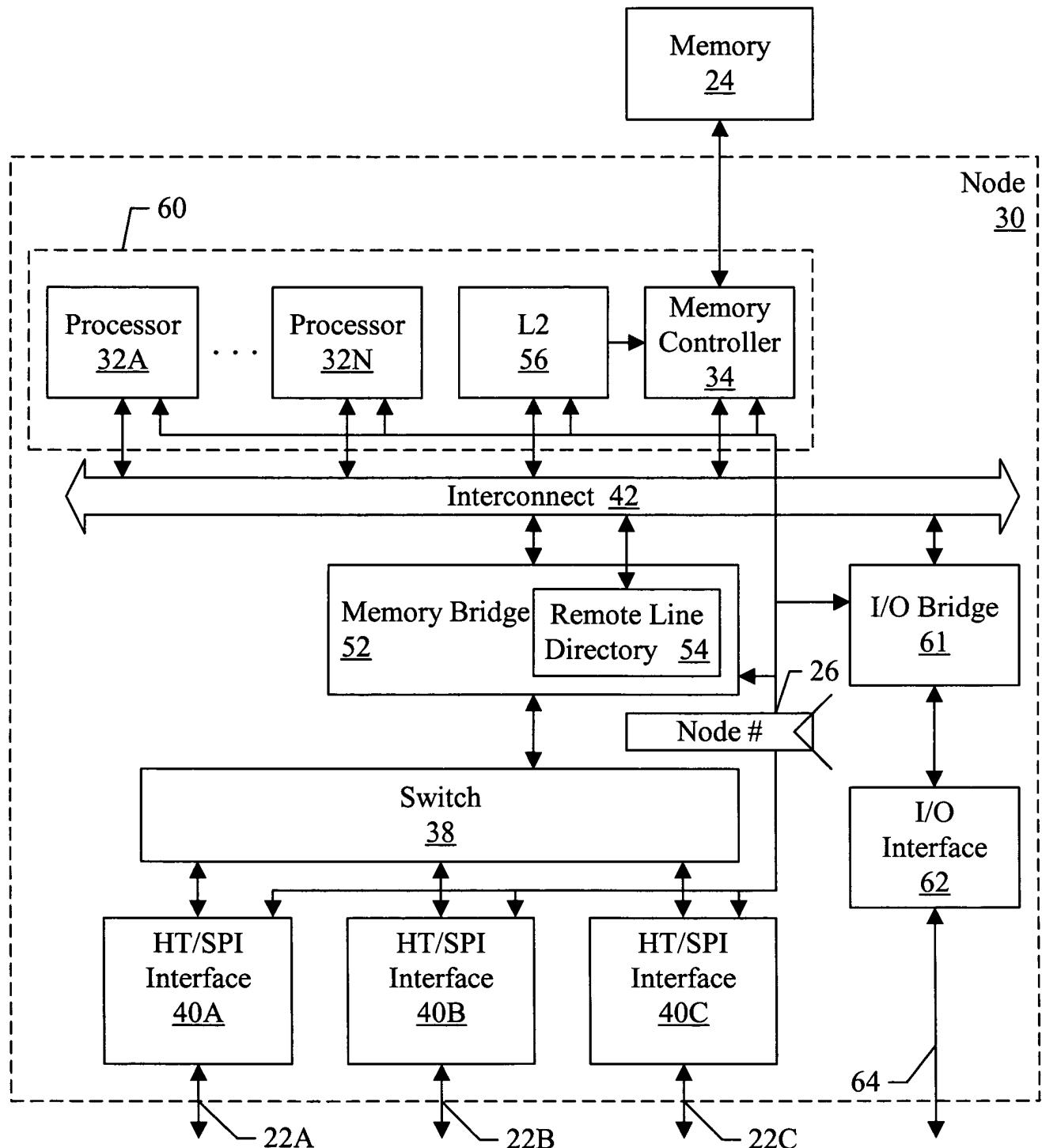


Fig. 3

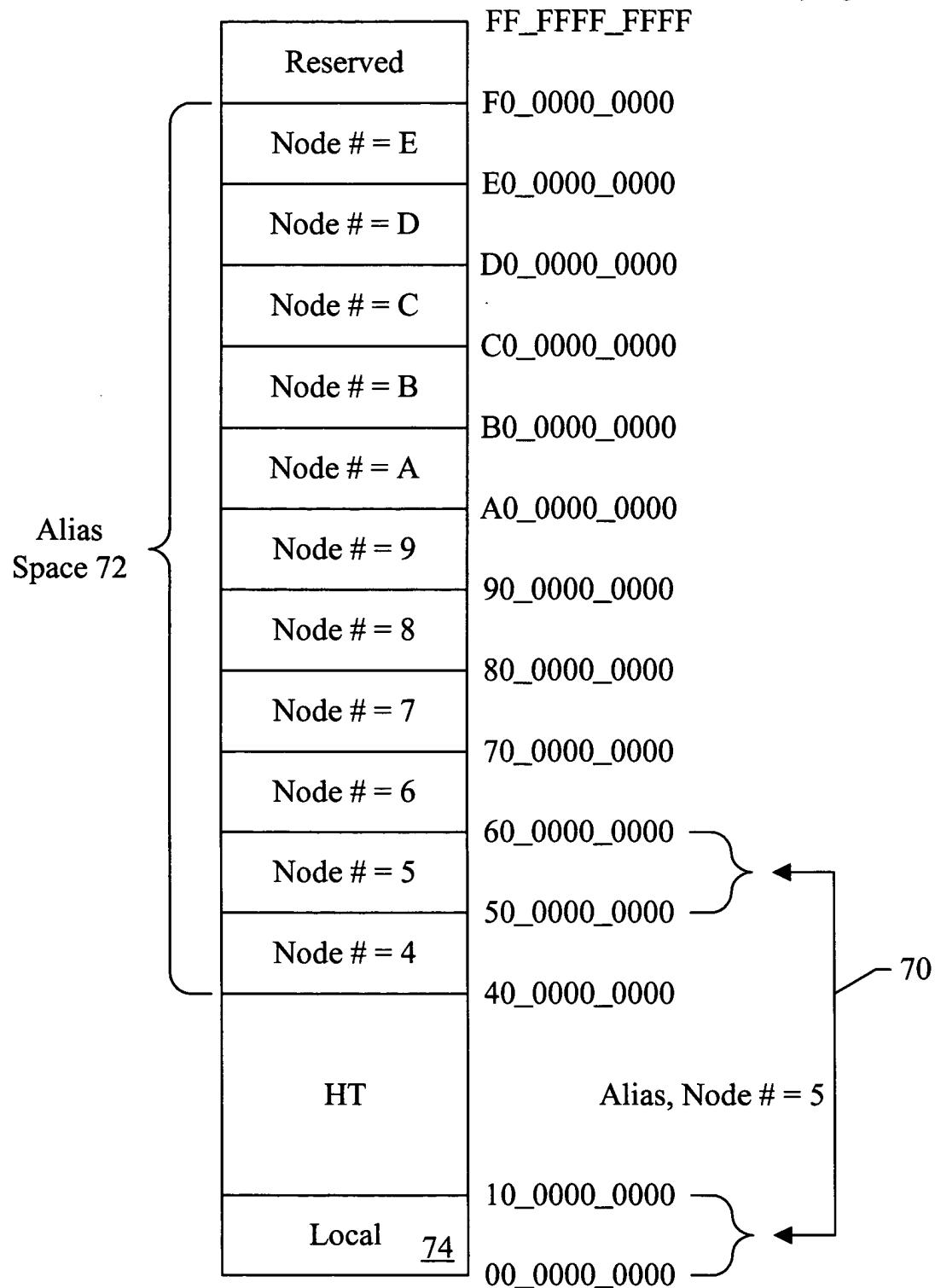


Fig. 4

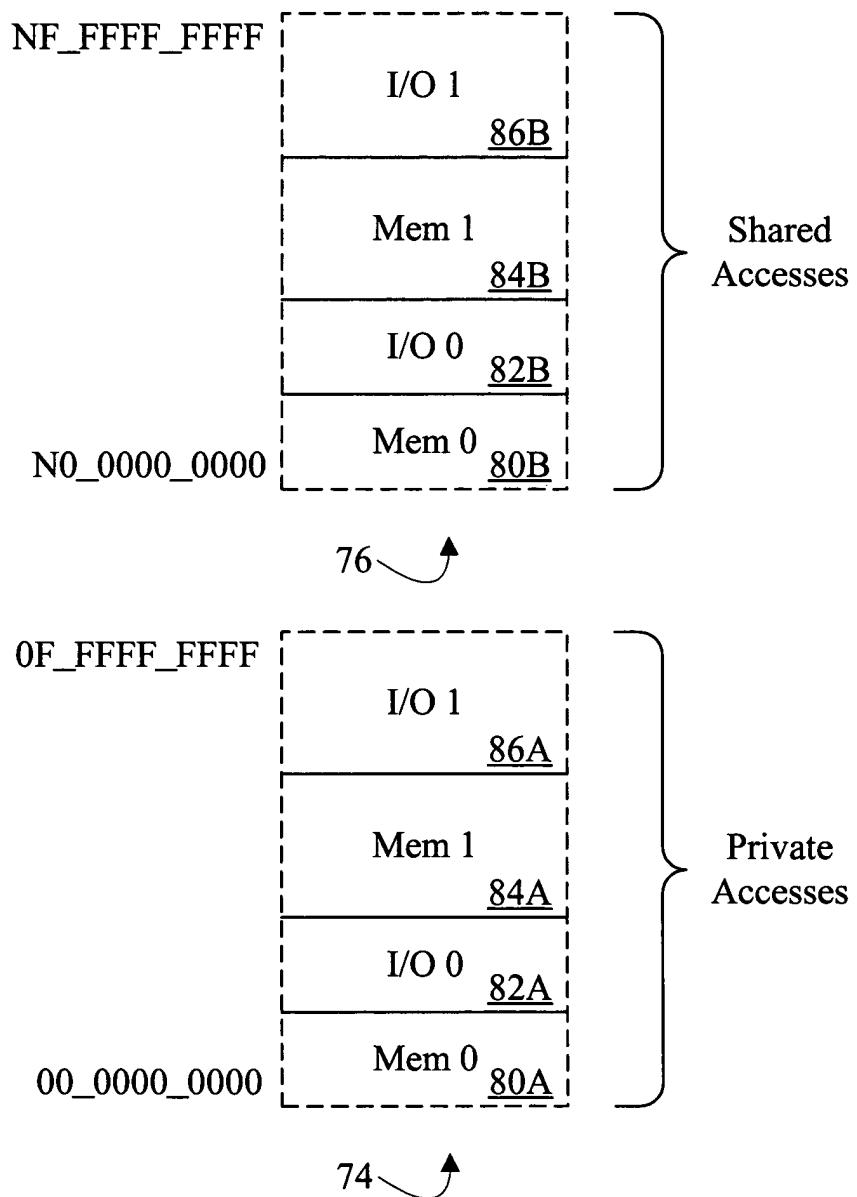


Fig. 5

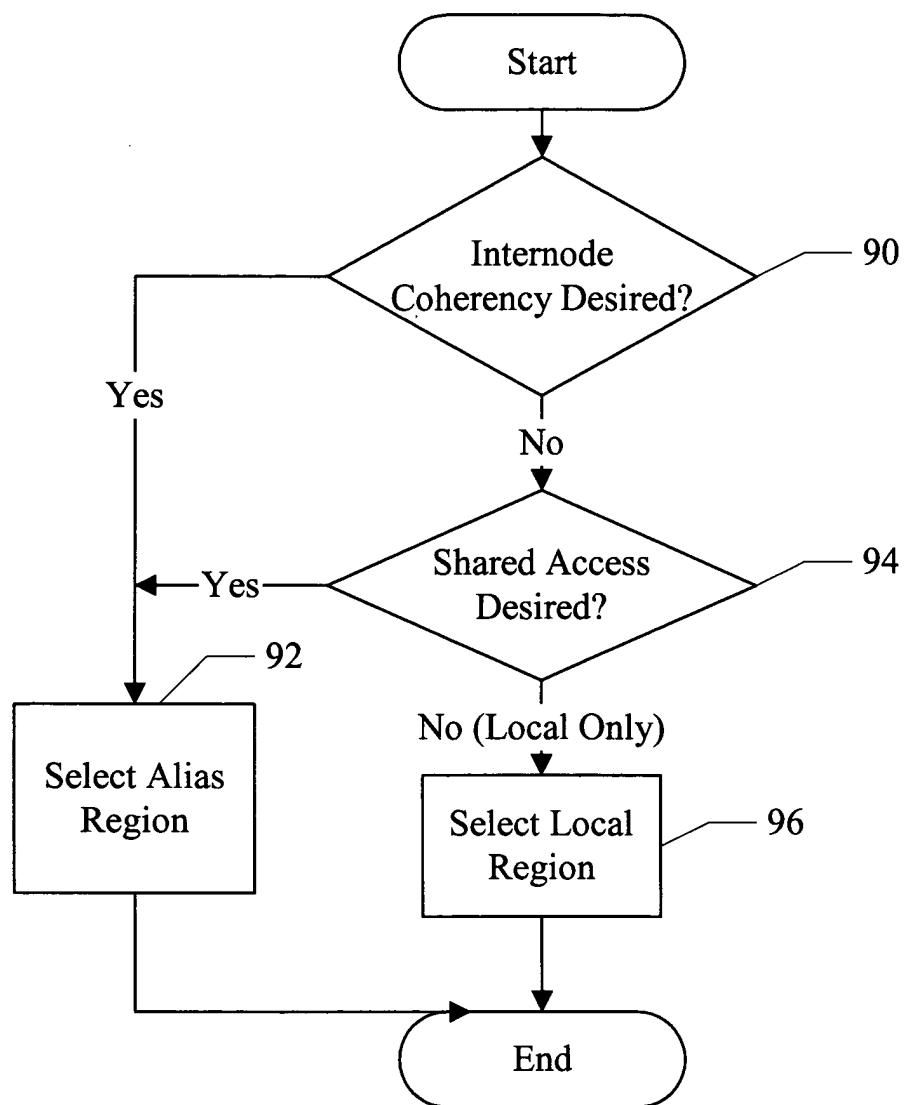


Fig. 6

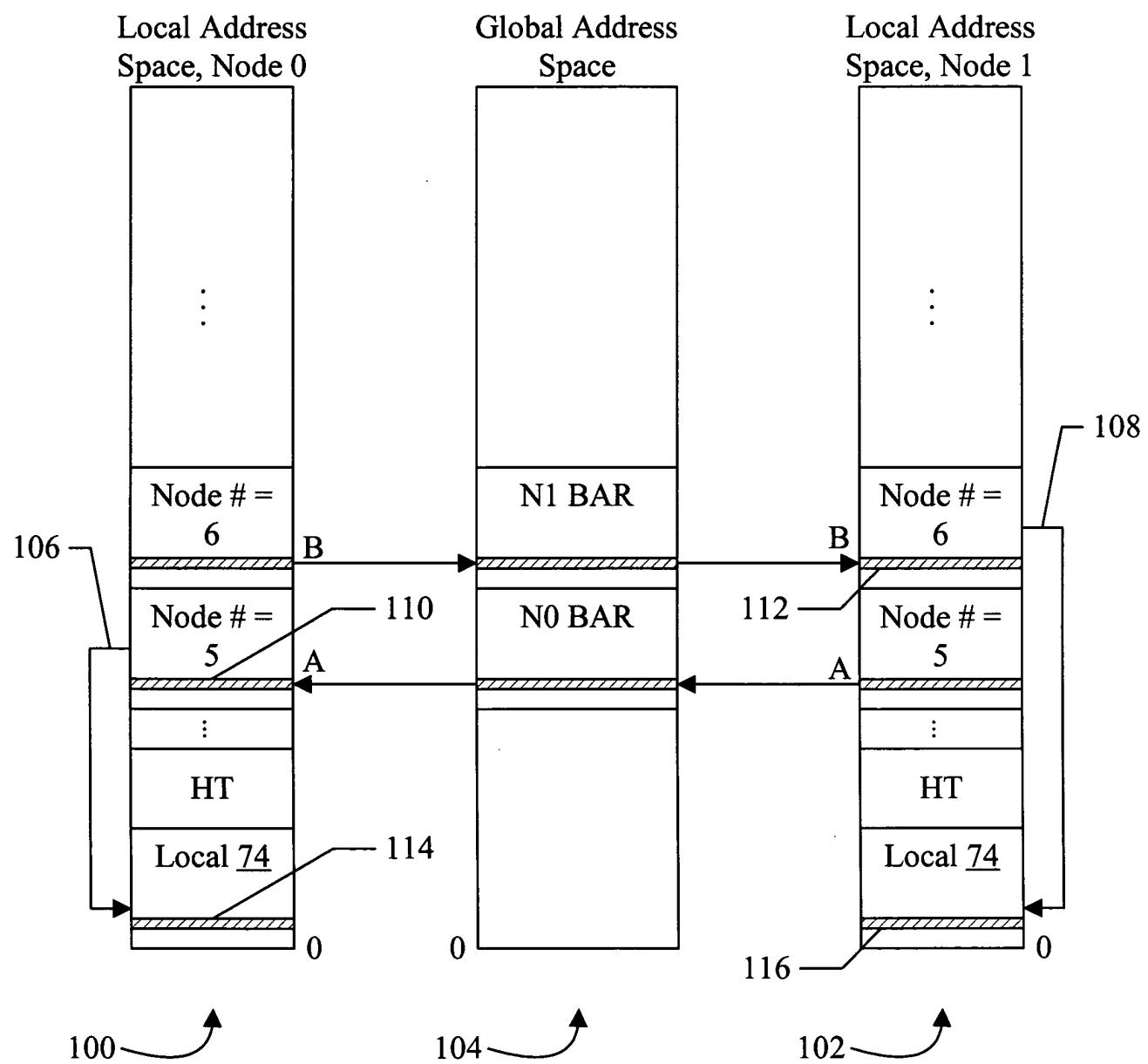


Fig. 7

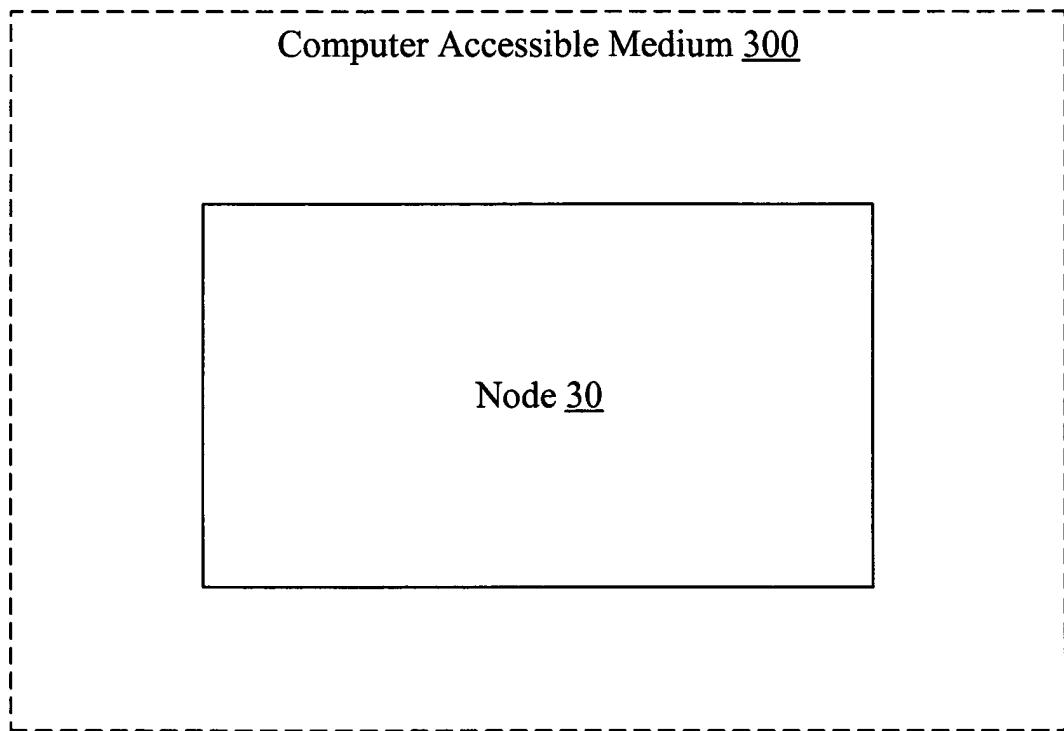


Fig. 8

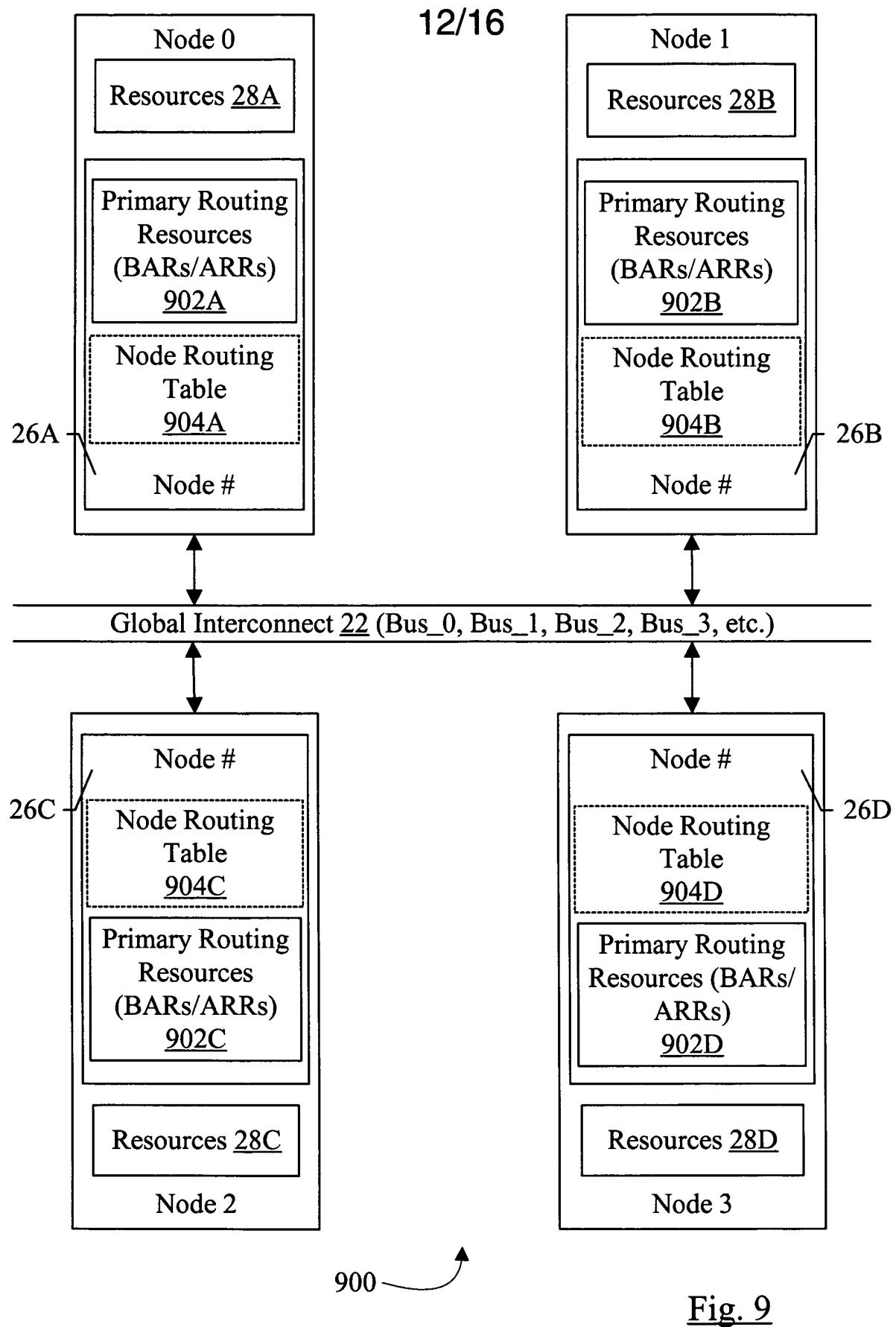


Fig. 9

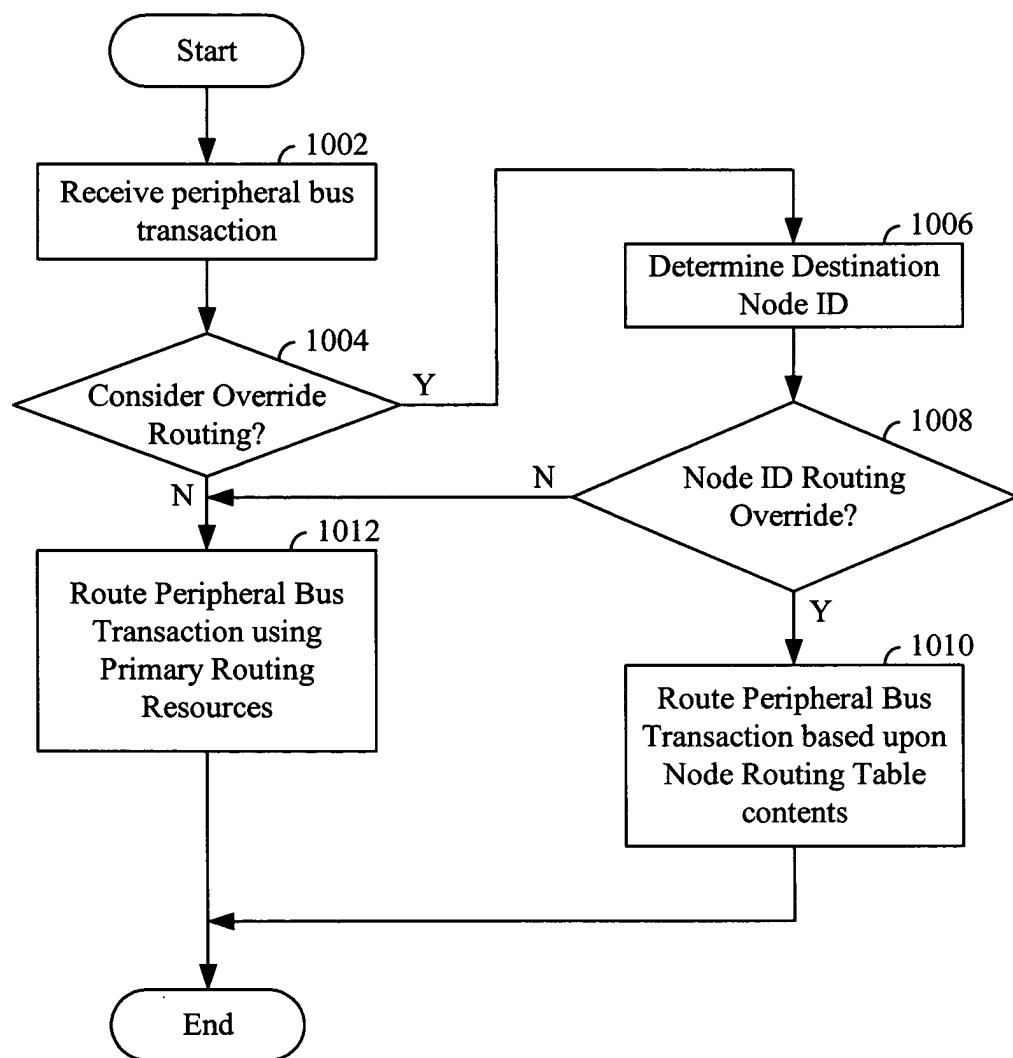


Fig. 10

MS-4 Addr.BitsNode Routing Table (Bridge A Entries) - 1100

MS-4 Addr.	Node	I/O Override bit	P/S	CC Override bit	P/S
0000	Node 0	I/O Override bit	P/S	CC Override bit	P/S
0001	Node 1	I/O Override bit	P/S	CC Override bit	P/S
0010	Node 2	I/O Override bit	P/S	CC Override bit	P/S
0011	Node 3	I/O Override bit	P/S	CC Override bit	P/S
0100	Node 4	I/O Override bit	P/S	CC Override bit	P/S
0101	Node 5	I/O Override bit	P/S	CC Override bit	P/S
0110	Node 6	I/O Override bit	P/S	CC Override bit	P/S
0111	Node 7	I/O Override bit	P/S	CC Override bit	P/S
1000	Node 8	I/O Override bit	P/S	CC Override bit	P/S
1001	Node 9	I/O Override bit	P/S	CC Override bit	P/S
1010	Node 10	I/O Override bit	P/S	CC Override bit	P/S
1011	Node 11	I/O Override bit	P/S	CC Override bit	P/S
1100	Node 12	I/O Override bit	P/S	CC Override bit	P/S
1101	Node 13	I/O Override bit	P/S	CC Override bit	P/S
1110	Node 14	I/O Override bit	P/S	CC Override bit	P/S
1111	Node 15	I/O Override bit	P/S	CC Override bit	P/S

Fig. 11

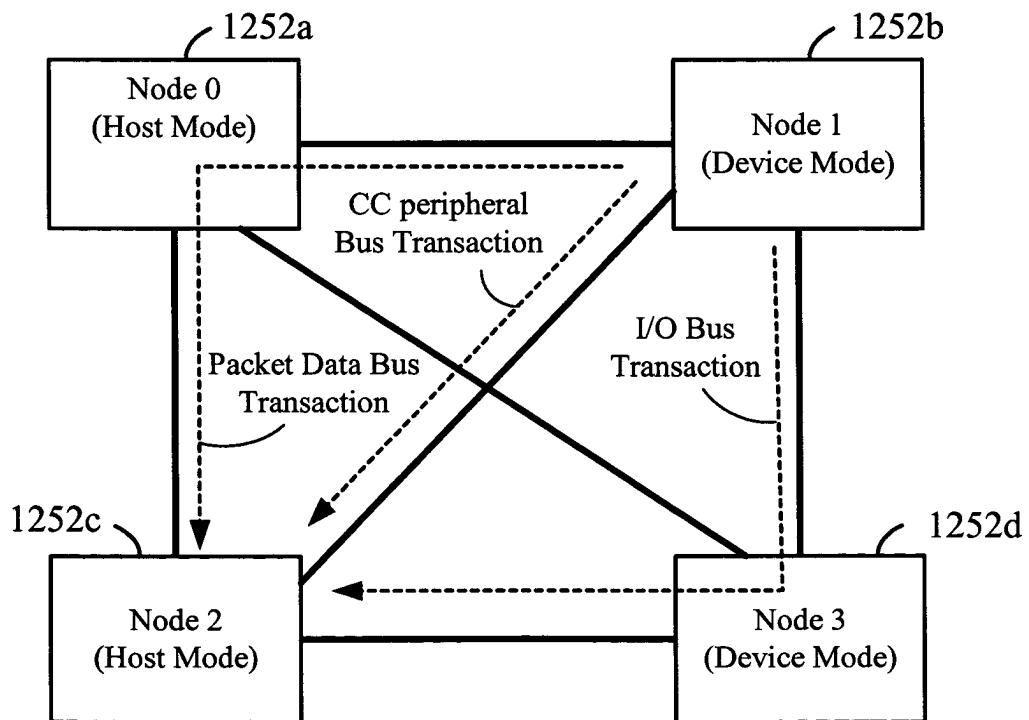
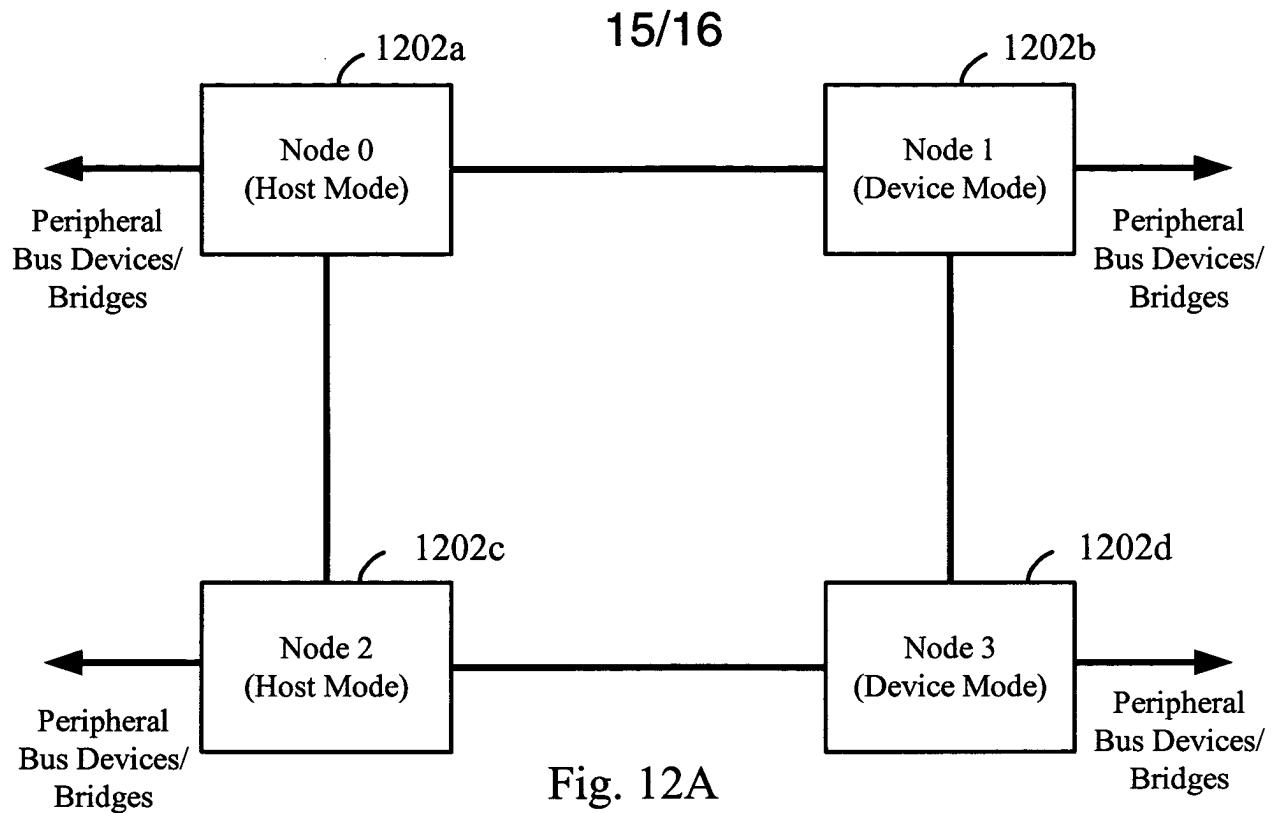


Fig. 12B

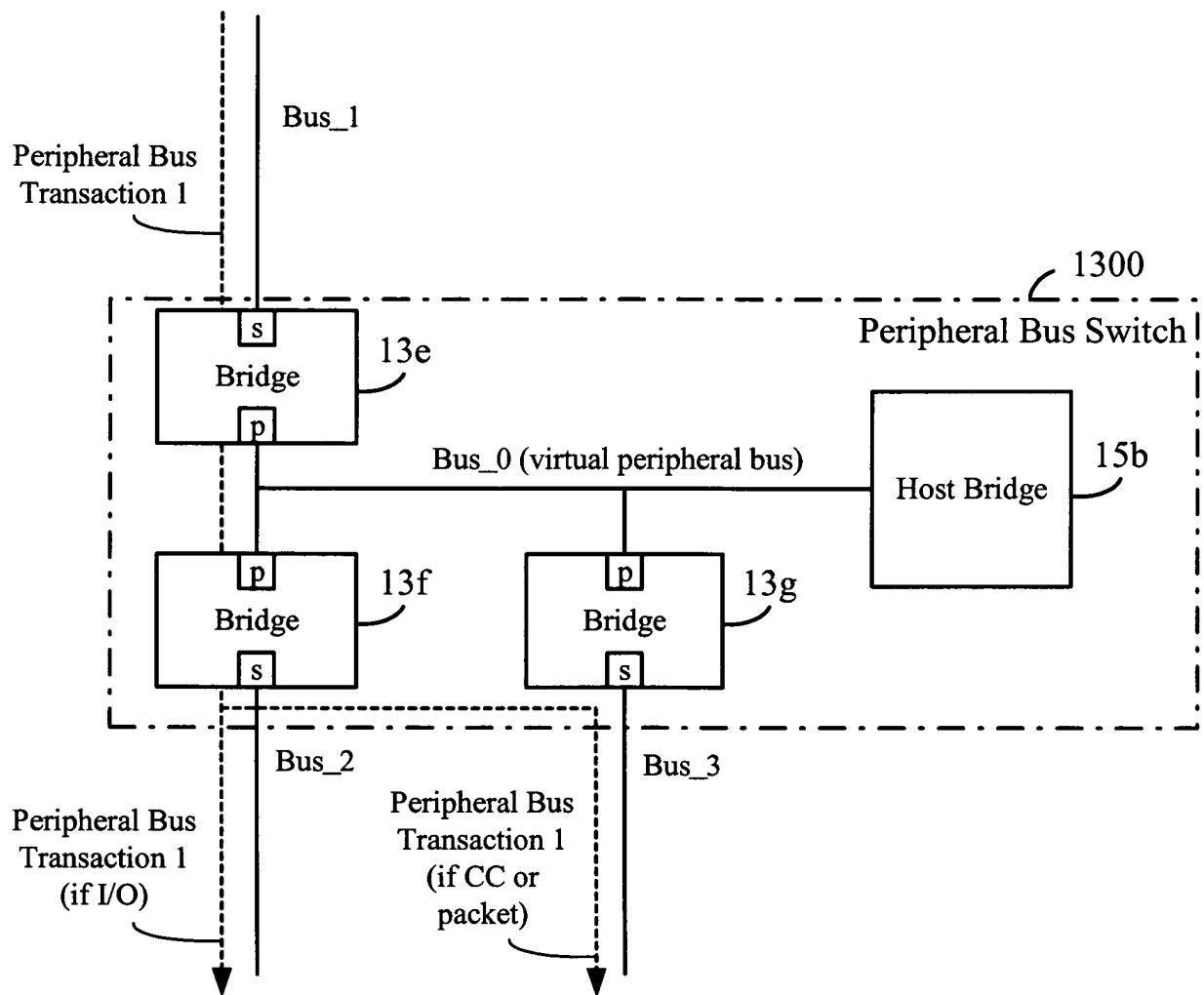


Fig. 13